

FIG. 1

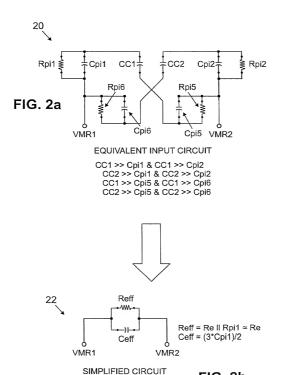


FIG. 2b

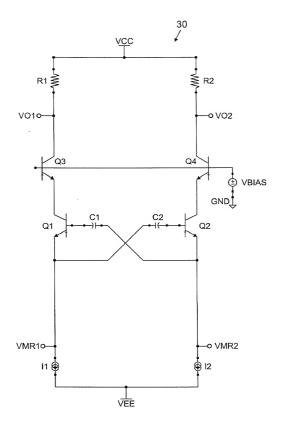
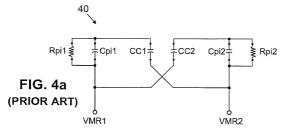
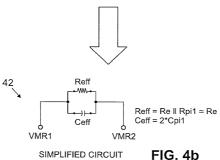


FIG. 3 (PRIOR ART)

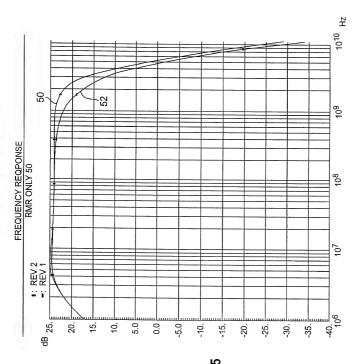


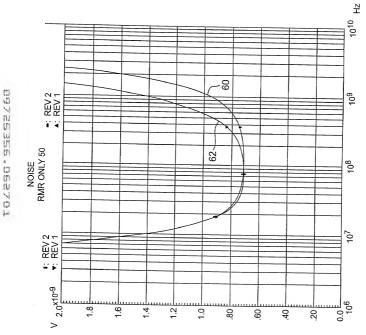
**EQUIVALENT INPUT CIRCUIT** CC1 >> Cpi1 & CC1 >> Cpi2 CC2 >> Cpi1 & CC2 >> Cpi2



(PRIOR ART)







-IG. 6